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Amendment B (contd)

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CLAIM AMENDMENTS

Claims 1-19 (Canceled).

Claim 20 (previously presented): A computing system comprising:

a CPU communicatively connected to a memory controller being communicatively connected to a memory connected either to an external operating system or to an internal operating system, through a switching unit;

an input unit that is able to input a request of switching;

a first status thereof defined by states of all variable registers thereof when said computing system is only connected to said internal operating system;

a second status thereof defined by states of said all variable registers when said computing system is only connected to said external operating system;

a switching unit being communicatively connected to said CPU and all said switches, respectively, has backups of both said statuses, and is able to backup a current status, control said general switch to control all said switches to interrupt all serving programs, then load the other status other than said current status to the computing system, wherein said switching unit has a trigger that is able to generate a non-maskable interrupt to said CPU after said switching unit receives a command of switching from said CPU responding to said request of switching, wherein a switch program is kept in said switching unit, and which has backups of both said statuses, and which, after said CPU receives said non-maskable interrupt, is able to backup a current status, control said general switch to control all said switches to interrupt all serving programs, then load the other status other than said current status to the computing system, and finally control said switching unit to reset said trigger; and

a supervising unit being able to monitor an execution process of said switch program running in said CPU, and allow said switch program to proceed if said supervising unit confirms said process has always been in a script of said switch program, or prohibit said switch control unit from any action if said supervising unit fails to confirm.

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Claim 21 (previously presented): A computing system comprising:

a CPU communicatively connected to a memory controller being communicatively connected to a memory connected either to an external operating system or to an internal operating system, through a switching unit;

an input unit that is able to input a request of switching;

a first status thereof defined by states of all variable registers thereof when said computing system is only connected to said internal operating system;

a second status thereof defined by states of said all variable registers when said computing system is only connected to said external operating system;

a switching unit being communicatively connected to said CPU and all said switches, respectively, has backups of both said statuses, and is able to backup a current status, control said general switch to control all said switches to interrupt all serving programs, then load the other status other than said current status to the computing system, wherein said switching unit has a trigger that is able to generate a non-maskable interrupt to said CPU after said switching unit receives a command of switching from said CPU responding to said request of switching, wherein a switch program is kept in said switching unit, and which has backups of both said statuses, and which, after said CPU receives said non-maskable interrupt, is able to backup a current status, control said general switch to control all said switches to interrupt all serving programs, then load the other status other than said current status to the computing system, and finally control said switching unit to reset said trigger, wherein said switch program has an ID verification unit thereof used to confirm a validity of a user, and making said switching unit either to proceed or stop respectively when identification is passed or failed.

Claim 22 (currently amended): A method of safely and quickly switching between an internal operating system and an external operating system for a computing system having a CPU, an input unit, a first status defined by states of all variable registers thereof when said computing system is only connected to said internal operating system, a second status defined by states of said all variable registers when

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said computing system is only connected to said external operating system, a switching unit, and[.,.] a switch program, wherein said method comprises following steps:

(1) ~~input~~ inputting a request of switching via said input unit when said computing system is in one of said ~~two~~ first and second statuses;

(2) after receiving said request of switching, running said CPU runs said switch program; and

(3) backing up said switch program ~~backe-up~~ a current status, ~~control~~ controlling said switching unit to interrupt all serving programs and ~~loads~~ loading the other status other than said current status to the computing system, wherein said switching unit has a trigger that is able to generate a non-maskable interrupt to said CPU after said switching unit receives a command of switching from said CPU responding to said request of switching, wherein said step (3) is that said supervising unit monitors an execution process of said switch program running in said CPU, and either (3a) allows said switch program to back up a current status, control said switching unit to interrupt all serving programs and loads the other status other than said current status to said computing system, and finally control said switching unit to reset said first trigger if said supervising unit confirms that said process has always been in a script of said switch program, or (3b) prohibit said switch control unit from any action if said supervising unit fails to confirm.